

S DEPARTMENT OF COMMERCE

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Washington, D.C. 20231

FIRST NAMED INVENTOR APPLICATION NO. FILING DATE ATTORNEY DOCKET NO 95-0653.02

08/984,562 12/03/97

MICRON TECHNOLOGY INC 8000 S FEDERAL WAY

BOISE ID 83706-9632

W ERIC WEBOSTAD

MAIL STOP 525

MAILLOUX

LMC1/0406

EXAMINER

KIM, H

ART UNIT

PAPER NUMBER

2751

DATE MAILED:

04/06/99

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary	Application No. Applicant(s) 08/984,562 Mailloux etal.
	Examiner Group Art Unit 275/
—The MAILING DATE of this communication appears on the cover sheet beneath the correspondence address—	
Period for Response	
A SHORTENED STATUTORY PERIOD FOR RESPONSE IS SET TO EXPIRE HALLING DATE OF THIS COMMUNICATION.	
 Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a response be timely filed after SIX (6) MONTHS from the mailing date of this communication. If the period for response specified above is less than thirty (30) days, a response within the statutory minimum of thirty (30) days will be considered timely. If NO period for response is specified above, such period shall, by default, expire SIX (6) MONTHS from the mailing date of this communication. Failure to respond within the set or extended period for response will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). 	
Status 1/2 - 1/2 G	
Responsive to communication(s) filed on $1/22/99$.	
☐ This action is FINAL .	
 Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 1 1; 453 O.G. 213. 	
Disposition of Claims	
Claim(s) 22-52	is/are pending in the application.
,	is/are withdrawn from consideration.
☐ Claim(s)	is/are allowed.
Claim(s) 22-32 is/are rejected.	
□ Claim(s)	
□ Claim(s)	
Application Papers requirement.	
☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.	
☐ The proposed drawing correction, filed on is ☐ approved ☐ disapproved.	
☐ The drawing(s) filed on is/are objected to by the Examiner.	
☐ The specification is objected to by the Examiner.	
☐ The oath or declaration is objected to by the Examiner.	
Priority under 35 U.S.C. § 119 (a)-(d)	
 □ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 11 9(a)-(d). □ All □ Some* □ None of the CERTIFIED copies of the priority documents have been □ received. □ received in Application No. (Series Code/Serial Number) □ received in this national stage application from the International Bureau (PCT Rule 1 7.2(a)). 	
*Certified copies not received:	
Attachment(s)	
☐ Information Disclosure Statement(s), PTO-1449, Paper No(s) ☐ Interview Summary, PTO-413
XNotice of References Cited, PTO-892	☐ Notice of Informal Patent Application, PTO-152
☐ Notice of Draftsperson's Patent Drawing Review, PTO-948	☐ Other
Office Action Summany	

U. S. Patent and Trademark Office PTO-326 (Rev. 3-97)

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Detailed Action

- 1. Claims 22-32 are presented for examination. This office action is in response to the Amendment filed on 1/22/99.
- 2. Receipt is acknowledged of information disclosure statements filed on 1/22/99 and 3/2/99, which the statements have been placed of record in the file. Information disclosed and listed on PTO 1449 ware considered.
- 3. The status of the related U.S. applications or patents should be updated and/or included as appropriate in the CROSS-REFERENCE TO RELATED APPLICATIONS section and in any other corresponding area in the specification, if any. (e.g., U.S. Patent Application Serial No. ##/###,### filled Sept. 07, 1990, now abandoned; ..., now U.S. Patent #,###,### issued Jan. 01, 1994; or This application is a continuation of Serial Number ##/###, filed on December 01, 1990, now abandoned; ...etc.)
- 4. It is noted that this application appears to claim subject matter disclosed in the copending section of this application. Applicants are reminded to maintain a clear line of demarcation between this application and co-pending applications to avoid possible double patenting.

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Specification

5. The objection to the title has been withdrawn because of the amendment.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 USC § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.
- 7. Claims 22-32 are rejected under 35 USC 102(b) as being anticipated by *Manning*, U.S. Patent 5,610,864.

As to claim 22, *Manning* discloses the invention as claimed. *Manning* discloses a memory circuit, comprises control logic (Fig. 1 Ref. 38), selection and temporary storage circuit (Fig. 1 ref. 18), and a multiplexer (Fig. 1 Ref. 40) for switching the memory circuit between a burst (col. 6 lines 14-26 and col. 7 lines 43-54) and a pipelined mode of operation (col. 5 lines 43-50).

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As to claims 23 and 24, Manning. further discloses external mode select signal and enable signal (Fig. 1 Ref 38).

As to claim 25, Manning. further discloses write enable and output enable (Fig. 2).

As to claim 26, Manning. further discloses a counter (Fig. 1 Ref. 26).

As to claim 27, Manning. further discloses the counter is used in the burst mode (col. 4 lines 47-49).

As to claim 28, Manning further discloses a second external address (col. 5 lines 43-50).

As to claim 29, Manning. further discloses EDO modes (col. 4 line 50).

As to claims 30 and 31, Manning further discloses CAS delay latency during a write and read cycle (Fig. 2).

As to claim 32, Manning further discloses an asynchronously accessible memory array (Fig. 1 and EDO constitutes asynchronous memory, col. 6 lines 14-16).

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Response to Amendment

8. Applicant's arguments with respect to claims 22-32 have been considered but are deemed to be most in view of the new grounds of rejection.

Applicant's argument on page 5 bottom that the reference does not disclose both a pipeline architecture and a burst architecture is not considered persuasive. *Manning* discloses that other memory architecture applicable to the current invention includes a pipelined architecture (Col. 5 lines 43-45) and the invention have a option of switching between burst and standard modes of operation (Fig. 1). Therefore, broadly written claims are disclose by the references cited.

Conclusion

- 9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
 - 1. USP 5,713,011, Jan. 27, 1998, Synchronized data processing system and image processing system; Jun Satoh, et al..
 - 2. USP 5,754,838, May 19, 1998, Synchronous dynamic memory device capable of operating over wide range of operation frequencies; Ken Shibata, et al..
- 10. A shortened statutory period for response to this action is set to expire 3 (three) months and 0 (zero) days from the mail date of this letter. Failure to respond within the period for response will result in **ABANDONMENT** of the application (see 35 USC 133, MPEP 710.02, 710.02(b)).

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11. Applicants are requested to number each line of each <u>claim</u> starting with line number one

to provide easier communication in the future.

12. When responding to the office action, Applicant is advised to clearly point out the

patentable novelty which he or she thinks the claims present in view of the state of the art

disclosed by the references cited or the objections made. He or she must also show how the

amendments avoid such references or objections. See 37 C.F.R. § 1.111(c).

13. When responding to the office action, Applicants are advised to provide the examiner

with the line numbers and page numbers in the application and/or references cited to assist

examiner to locate the appropriate paragraphs.

14. Any inquiry concerning this communication or earlier communications from the

Examiner should be directed to Hong Kim whose telephone number is (703) 305-3835. The

Examiner can normally be reached on the weekdays from 8:30 AM to 5:00 PM.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's

supervisor, Eddie Chan, can be reached on (703) 305-9712.

Any inquiry of a general nature or relating to the status of this application should be

directed to the Group receptionist whose telephone number is (703) 305-3900.

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15. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks Washington, D.C. 20231

or faxed to:

(703) 308-9051-2, (for formal communications intended for entry)

Or:

(703) 305-9731 (for informal or draft communications, please label "PROPOSED" or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington. VA., Sixth Floor (Receptionist).

HK

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Patent Examiner April 2, 1999

EDDIE P. CHAN
SUPERVISORY PATENT EXAMINER

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